IN THE SPECIFICATION:

Please replace the title of the invention with the following amended title:

Apparatus and Method for Swapping-Out Real Memory by Inhibiting I/O Operations to a Memory Region and Setting a Quiescent Indicator, Responsive to Determining the Current Number of Outstanding Operations

Please replace the paragraph appearing on page 33, lines 10-25 of the specification, with the following amended paragraph:

-- Memory management is the process whereby this information is used to verify the access rights for this transaction and to determine the real addresses to be used for the data transfer. The access rights include but are not limited to the following checks: the data segment defined above must fall within the memory region, if a receive operation is performed on the memory region, the memory region must allow write access, the protection domain of the QP must be the same as the protection domain of the memory region, and the like. A more detailed description of access rights is provided in commonly assigned and co-pending U.S. Patent Application Serial No. [[_____]] 09/758,788 (Attorney Docket No. AUS9-2000-640US1), entitled "METHOD AND APPARATUS FOR MANAGING ACCESS TO MEMORY," filed [[_____]] November 11, 2001, which is hereby incorporated by reference. --

Please replace the paragraph appearing on page 33, line 31 to page 34, line 6 of the specification, with the following amended paragraph:

-- Figure 12 illustrates a two-table memory management scheme described in the incorporated U.S. Patent Application Serial No. [[_____]] 09/758,788 (Attorney Docket No. AUS92000640US1). As shown in Figure 12, the protection table 1210 is indexed by a portion of a Local Key (L_Key) or Remote Key (R_Key), i.e. key index 1220, to access the protection table entry 1212 that is associated with a given memory region 1230. --

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